



INFORMATION DISCLOSURE				ATTY. DOCKET NO.		SERIAL NO.		
CITATION IN AN				57454-072				or all
APPLICATION								. <u>4</u>
				APPLICANT Shigeki OHRAVASHI				
				Singeri Olibit 173511				
(PTO-1449)				FILING DATE April 10, 2001		2818 5		
U.S. PATENT DOCUMENTS								
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME		CLASS	SUBCLASS	FILING DATE	
JY.	5,808,930	9/15/98	Wada et al.		365	63		
AM	6,088,276	7/11/00	Ukita		365	207		
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FOREIGN PATENT DOCUMENTS								
EXAMINER'S INITIALS	PATENT NO.	DATE	CO	UNTRY	CLASS	SUBCLASS		Translation
							Yes	No
Str.	5-63091	3/12/93	<del></del>	English abstract)				
* .	9-162305	6/20/97	<del>                                     </del>	English abstract)				
JVV /	11-306762	11/5/99	<del>                                     </del>	English abstract)				
W	9-270468	10/14/97	Japan (with E	English abstract)			<u> </u>	
	<u> </u>							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
Ju.	"A 14-ns 1-Mbit CMOS SRAM with Variable Bit Organization", by Kohno et al., IEEE Journal							
<u> </u>	of Solid-State Circuits, Vol. 23; No. 5 (October 1988), pp. 1060-1066.							
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EXAMINER DATE CONSIDERED								